XAVIER INSTITUTE OF ENGINEERING

Mahim, Mumbai 400016 Department of Information Technology

(NBA Accredited)

(Approved by AICTE, Govt. of Maharashtra and Affiliated to University of Mumbai)

Title of the event: Poster Making Competition

Event Date / Time: 31st March 2023 at 1:30 pm

Event Coordinators: Ms. Meena Ugale

Details of Resource Person: -

No. of Participants: 67

Class/Semester: SE/IV

Objectives:

The objective behind organization of poster making competition is to bring out the creative expression of students and to gauge their knowledge and awareness of various trends in Computer Organization & Architecture.

The aim of the Poster Making Competition is not just to judge the students based on their creative aspects but also to trigger thought process among the students towards recent trends in the field of Computer Organization & Architecture.

Description:

The poster making competition is organized on 31st March 2023. The students formed a group of 3 to 4 and selected the topics. They prepared A3 size poster and presented it.

Judgement criteria was based on Poster content, Poster Organization and Delivery of the content.

Total 18 groups participated in this event and top three groups awarded prize of Rs. 500/ each.

| Group No. | Roll No. | Name | Торіс |
|--------------|-------------|------------------|-------------------------------|
| 1 | 2 | Aditiya Nalawade | RISC & CISC |
| | 26 | Khushal Jogi | |
| | 33 | Piyush Mane | |
| : | 35 | Ajay Pandey | |
| | 3 | Aniket Badgujar | A11 Bionic Processor |
| 2 | 25 | Saket Jha | |
| | 32 | Ajay Kurien | |
| 3 | 9 | Jovan Creado | Architecture of ARM Processor |
| | 11 | Rajaram Desai | |
| | 14 | Vedant Gharat | |
| | 28 | Jayesh Kakade | |

| 4 | 36 | Mangesh Pangam | |
|--|-------------|--------------------------------|--|
| | 40 | Saish Rane | ARM7 |
| | 43 | Rakshita Sarap | |
| 5 | 49 | Harsh Sharma | |
| | 53 | Sumedh Shinde | Total A. 122 a. B. Br. 32 |
| | 61 | Sumit Vishwakarma | Intel Architecture Processors Pipeline |
| | 64 | Belona Cerejo | |
| 6 | 70 | Nelson Kolas | How Processors are manufactured? |
| | 67 | Sanuel Pallikonda | |
| | 68 | Brice Pimenta | |
| | 23 | Rohan Jaiswal | |
| | 24 | Yash jaiswar | Memory Organization |
| 7 | 27 | Ronak Joshi | |
| ļ | 34 | Vedang Nijap | 1 |
| | 44 | Aditi Satam | |
| - | 51 | Aditiya Shinde | |
| 8 | 55 | Pratham Singh | Dedicated vs Integrated Graphics Cards |
| | 56 | Shreya Singh | 1 |
| | 69 | Khizar Shaikh | |
| 9 | 65 | Gaurav Kashelkar | Microprocessors Security & Encryption |
| | 66 | Siddhi Oswal | Techniques |
| | 46 | Fradeen Shaikh | |
| 10 | 47 | Anas Shaikh | RAID |
| 10 | 48 | Mohtahsim Shaikh | I III |
| | 29 | Riya Kamble | |
| | 37 | Shruti Parade | Embedded Systems |
| 11 | 39 | Lekha Pulavarthy | |
| | 42 | Anshu Sakhare | |
| | 8 | Parth Chaudhary | |
| | 16 | Harshvardhan Gupta | Intel vs AMD processors |
| 12 | 19 | Sejal Gupta | |
| | 22 | Harshit Jain | - |
| | 3 | Siddhi Awlegaonkar | |
| | 13 | | Quantam Computing |
| 13 | 21 | Aditya Ghadge Shreya Jadhav | |
| | | Sacchidanand Yadav | |
| · | 63 | | Future trends in Bus Technology |
| 14 | 45 | Akib Sayed Shubham Sharma | |
| | 50 | | |
| | 59 | Ishan Vaghela | |
| | 62 | Neha Yadav | |
| | 4 | Mayuresh Balsaraf Shivam Sahu | History of Intel Processors |
| 15 | 41 | | |
| | 57 | Vivek Singh | |
| | 60 | Prathamesh Vaidya | |
| 16 | 6 | Vedant Chaudhari | Instruction set of Pentium Processor |
| | 10 | Rupesh Darpe | |

| | 12 | Divyajothi Raja | |
|----|----|-------------------|---------------------|
| | 15 | Salil Gujar | |
| 17 | 18 | Saksham Gupta | Havard Architecture |
| | 7 | Saahil Chaurasia | |
| | 17 | Pratik Gupta | |
| ĺ | 20 | Steve Hetya | |
| 18 | 38 | Piyush Singh | Assemblers |
| | 52 | Siddharth Shinde | |
| | 54 | Kunal Singh | |
| | 58 | Gauresh Somwanshi | |

The winners of this competition are as follows.

| Group No. | Roll No. | Name of Student | Topic |
|--------------|----------|-------------------|--------------------------------------|
| 11 | 29 | Riya Kamble | Embedded Systems |
| | 37 | Shruti Parade | |
| | 39 | Lekha Pulavarthy | |
| | 42 | Anshu Sakhare | |
| 15 | 4 | Mayuresh Balsaraf | History of Intel Processors |
| | 41 | Shivam Sahu | |
| | 57 | Vivek Singh | |
| | 60 | Prathamesh Vaidya | |
| 16 | 6 | Vedant Chaudhari | Instruction set of Pentium Processor |
| | 10 | Rupesh Darpe | |
| | 12 | Divyajothi Raja | |
| | 15 | Salil Gujar | |

COs / LOs achieved (if any): -

POs achieved: PO1, PO2, PO8, PO9, PO10, PO12

PSOs achieved: PSO1, PSO2

Event Photos: